
ORION[®]

This week we have continued on coding base classes.

Ilgin YARIMAĞAN

This week I started working on the printing section of our project. Actually I reviewed the part I developed for the demo and then redesigned considering in a more detailed way. Basically I converted the image object returned by the drawing panel (of Jgraph object) to a pdf file since pdf is a Printable in java. I will continue working on the Print2dtoStream class in order to generate the actual print job of the pdf file saved from the current status of drawing area. I also worked on some details about gui.

M. Ergin SEYFE

Last two weeks, I worked on simulation again. All communication classes between the design and simulation is almost done. Now, I will start to simulate sequential gates. Meanwhile, I try to find some different algorithms which will increase the speed of simulation but JHDL does not allow it. So, I return back to the old one. But, I am still trying other ways to simulate gates, which will be system friendly.

Eren YILMAZ

In these two weeks, I worked on some fixes in the current classes. First, me and Emin worked on the connection ports of components and standardized the port structure in the ComponentGraphCell class. Then, I began searching some different line routing algorithms for the JGraph drawing area, since the default routing algorithm is somewhat problematic. This algorithm only breaks the line in the middle, regardless of existing lines and/or components. I searched through the Internet but could not find any other algorithms in the first search. It seems that we will implement a better line routing algorithm.

I also helped Emin in correcting some points in the JGraph drawing panel.

Emin ÖZCAN

In these two weeks, I worked on the line drawing and component drawing. Line Drawing and connecting to the components has been finished. Basic components can now be created by drag and drop from the left menu. All lines and components are hold in line_hashtable and comp_hashtable,respectively. The only problem is disconnecting line from a component. I think, I will solve this problem in a few days. Moreover,I created a structure for the simulation. This structure hold the information about ,which line is connected to the which component,which line is connected to the which port of the component,all required information. Then i send it to Ergin for making it easy to work with simulation.

Mehtap Ayfer PARLAK

For two weeks I interested in our component and other icons. I have searched for suitable images in order to provide a more professional gui design since we are trying to fix our images. Meanwhile I am continuing to search Diglog file format.