
ORION®

This week we have continued on coding base classes.

Ilgin YARIMAĞAN

This week I started working by examining Serializable interface and how to implement this interface in order to serialize our DrawingPanel, and Component and Line objects. This serialization and deserialization process provides a way to save/load our program. I managed to serialize/deserialize the internal structure using Component and Line objects and the current state of the program (line size, grid color) using the DrawingPanel object. We also continue to work on the load process by redrawing the obtained objects on the Drawing Panel using Jgraph together with Emin.

M. Ergin SEYFE

This week, I worked on simulation and add new features like simulation of new gates also I worked on the integration of the design package with simulation package. Then I tested the simulation classes because our data structure of connection classes had been changed, after all I found a lot of errors and bugs. I fixed all of them and now it works properly. Also previously, I calculate the simulation value for only the output labels, but now simulation class calculates and returns the all values of the gates which enable the glow mode in drawing.

Eren YILMAZ

This week, I first worked on line routing. I searched the Internet for some routing algorithms, but the ones I found were not suitable to our project, so I wrote a simpler algorithm. After finishing it, I began working on simulation thread. I tried to do it to enable on-the-fly simulation, but yet, it has not been finished. The simulation is now semi-static, if any input changes, the simulation runs and updates the values. This week, I also tried to standardize the names of components by creating the Library class.

Mehtap Ayfer PARLAK

This week again I continued to search Diglog. In order to understand some parts that I could not infer by reverse engineering, I have send an email to the programmers of Diglog. He explained some basic issues to me and advised to look at logspc.c and logntk.c files from the Diglog src file. I tried to understand these files but they only concern with the netlist information, they take the diglog schematic and convert it to the ntk or spice code in order to simulate. But we want to go back and forth between our tool and diglog. So we must understand the geometric information of schematics also. Hence I am trying to solve how the geometric information of lines are stored. I solved the basic structure generally. Only some details left.

Emin ÖZCAN

This week, I focused on giving special color to lines and input/output components before and after simulation. To implement this, I changed the ini files of input/output components and reload their images at every time the simulation is done. After this, I worked with Ilgın to complete the save and load feature of our project. Ilgın returns me hash tables of lines and components, and I reload all lines and components to the drawing area. The save/load feature is nearly finished.