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DigART

DIGITAL CIRCUIT SIMULATOR PROJECT

Weekly Report 3

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ST★R SOFT	**DigArt Project Weekly Report 1**	**DA002**	**1.1**

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1. WHAT WE HAVE DONE

1.1. HALİT DEVELİOĞLU

My duty for the week was collecting information about Hardware Description Languages HDL and VHDL. During my research, I have tried to find how we can integrate these languages to our project which will be developed using Java technology. After some researches I found a java extension of HDL, named as JHDL. It is free and open source and I believe that it is something that we were looking for.

1.2. ELİF SAYGI

My duty for this week was collecting information about script support for DigArt project. I have investigated the possibilities about what can be included within script support by searching similar projects and foreseeing the our project's needs. I have also examined the last year's Software Requirement Analysis Reports in order to have an idea what can we include additionally to our report.

1.3. SERHAT KOYULMUŞ

This week, I searched what is hdl (hardware description language), vhdl, jhdl vs. I think that jhdl is important for our project since we will use java programming language. It will be also beneficial for script support. I found tutorials about jhdl and read them. There are many test builders on the web for circuits written with hdl. Most of them are open source. It will be also usefull to look their source codes. Maybe we can use them.

1.4. ŞENİZ SÖZER

This week I was responsible to search about script support. I have investigated some not related programmes that have script support and tried to have an idea about how can we do it for our project.

1.5. VOLKAN İÇEL

My duty for this week was searching development environments and deciding about their advantages and disadvantages.