DigART

DIGITAL CIRCUIT SIMULATOR PROJECT

Weekly Report 5

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WHAT WE HAVE DONE

We have came together at 10.11.05. We have revised the document "Software Requirements Analysis Report" for the meeting. We have discussed about some development strategies for the "Initial Software Design Report". We selected some research topics for us and assigned Action Items to group members;

- JHDL will be investigated in detail by Serhat Koyulmus, Halit Develioglu and Volkan Icel.

- Java2D will be investigated in detail by Elif Saygi and Seniz Sozer. Then, a discussion will be done either to select OpenGL or Java2D, or any alternatives found, to implement graphical representation of DigART project.

- Everyone will make a research and give a comment about development environment and tool till the next meeting.

We have also discussed about our meeting schedule.