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DigART**DIGITAL CIRCUIT SIMULATOR PROJECT****Weekly Report 6****Number of Pages****Doc****Appendixes**

2

A

B

C

D

E

F

G

H

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WHAT WE HAVE DONE

This week we have completed Ceng477's project phase2. So, we can not have found enough time to complete all action items on us. We have read JAVA tutorials and studied to come familiar its syntax. We have downloaded the documents from www.jhdl.org about JHDL and started to study on it.