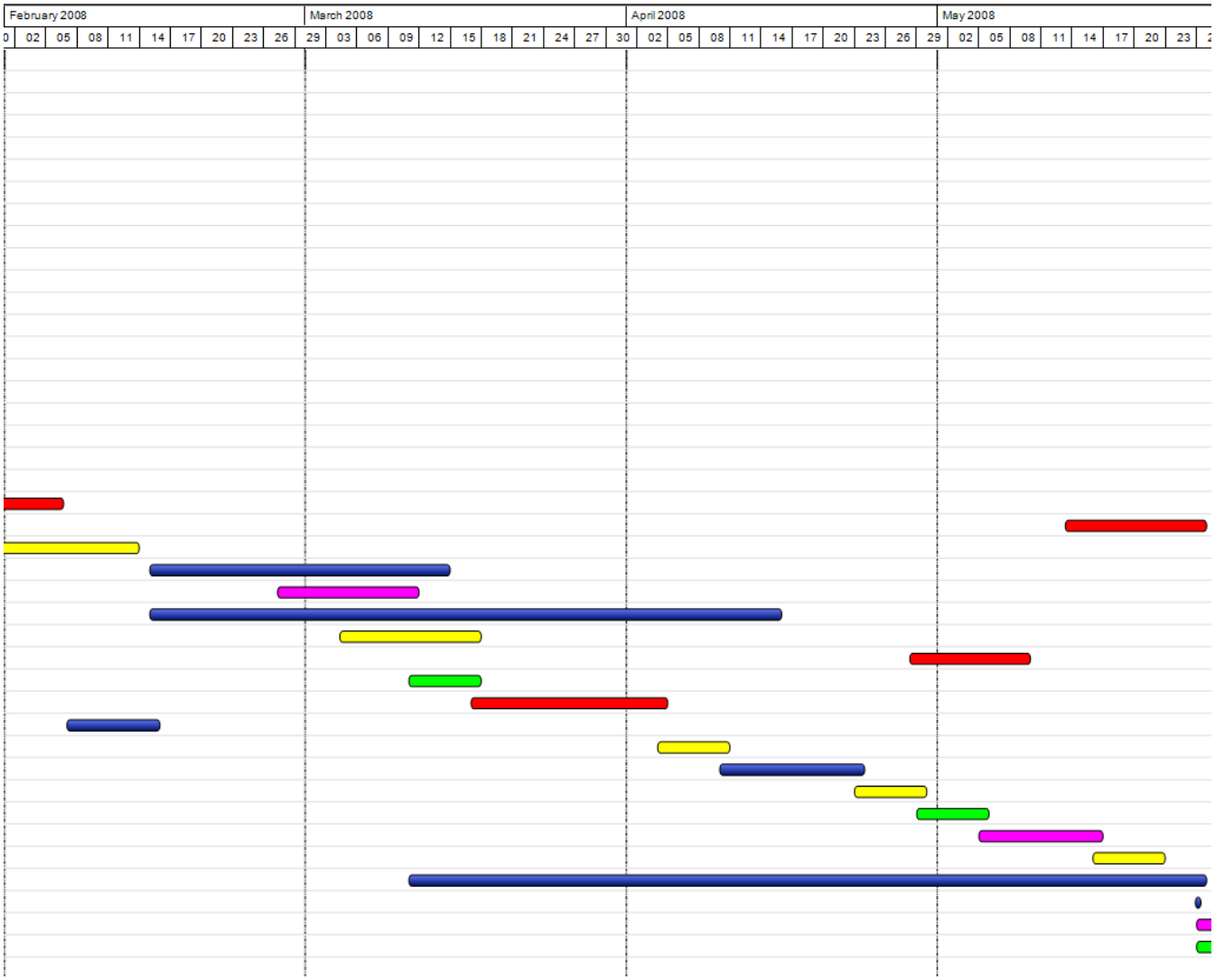#	Name	Start	Finish
1	PLANNING	9/21/2007	10/4/2007
2	REQUIREMENT ANALYSIS	10/16/2007	10/29/2007
3	Researches on Specifications	9/27/2007	10/10/2007
4	Meeting and Analysis of inspenctions	10/3/2007	10/16/2007
5	Requirement Specifications	10/16/2007	10/29/2007
6	Milestone	10/29/2007	11/9/2007
7	INITIAL DESİGN	11/6/2007	11/19/2007
8	Working on drafts	10/25/2007	11/7/2007
9	Working on specifications	10/31/2007	11/13/2007
10	Initial design specifications	11/6/2007	11/19/2007
11	Milestone	11/19/2007	11/30/2007
12	DETAILED DESIGN	1/11/2008	1/24/2008
13	Detailed design specifications	11/12/2007	11/23/2007
14	Complete class diagram	11/16/2007	11/29/2007
15	Hardware design	11/29/2007	12/12/2007
16	Software design	12/5/2007	12/18/2007
17	User interface design	12/17/2007	12/28/2007
18	Library design	1/1/2008	1/14/2008
19	Tests	1/7/2008	1/18/2008
20	Design specifications	1/11/2008	1/24/2008
21	Milestone	1/24/2008	2/6/2008
22	IMPLEMENTATION	5/13/2008	5/26/2008
23	Working on pre implementations	1/31/2008	2/13/2008
24	Board design	2/15/2008	3/14/2008
25	Working on card	2/27/2008	3/11/2008
26	Serial port communication	2/15/2008	4/15/2008
27	Review	3/4/2008	3/17/2008
28	Implementation details	4/28/2008	5/9/2008
29	Software implementation on card	3/11/2008	3/17/2008
30	Library implementation	3/17/2008	4/4/2008
31	Working on script	2/7/2008	2/15/2008
32	Code implementation for smart card	4/4/2008	4/10/2008
33	Code implementation for ports	4/10/2008	4/23/2008
34	Code implementation for other parts of	4/23/2008	4/29/2008
35	Performance working	4/29/2008	5/5/2008
36	Tests and feedbacks	5/5/2008	5/16/2008
37	Final implementations	5/16/2008	5/22/2008
38	Documentation	3/11/2008	5/26/2008
39	Milestone	5/26/2008	5/26/2008
40	USER EVALUATİON	5/26/2008	5/30/2008
41	Feedback	5/26/2008	5/30/2008
42	Milestone	5/30/2008	5/30/2008